

## Refine Search

### Search Results -

Terms	Documents
cmos same (mobility near5 match\$5)	4

**Database:**

- US Pre-Grant Publication Full-Text Database
- US Patents Full-Text Database
- US OCR Full-Text Database
- EPO Abstracts Database
- JPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

**Search:**

L12

### Search History

DATE: Friday, July 08, 2005 [Printable Copy](#) [Create Case](#)

<u>Set</u>	<u>Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
side by side				result set
DB=USPT; PLUR=YES; OP=OR				
<u>L12</u>	cmos same (mobility near5 match\$5)		4	<u>L12</u>
DB=TDBD; PLUR=YES; OP=OR				
<u>L11</u>	cmos and mobility and aspect ratio	L10	0	<u>L11</u>
<u>L10</u>	L9		0	<u>L10</u>
DB=DWPI; PLUR=YES; OP=OR				
<u>L9</u>	L8		0	<u>L9</u>
DB=JPAB; PLUR=YES; OP=OR				
<u>L8</u>	L7		0	<u>L8</u>
DB=EPAB; PLUR=YES; OP=OR				
<u>L7</u>	L5		0	<u>L7</u>
<u>L6</u>	L5		0	<u>L6</u>
DB=PGPB; PLUR=YES; OP=OR				
<u>L5</u>	L4		0	<u>L5</u>

*DB=USPT; PLUR=YES; OP=OR*

<u>L4</u>	CMOS and (((NMOS near5 PMOS)near5 (aspect ratio)) same (equal\$3 near3 mobility))	0	<u>L4</u>
<u>L3</u>	CMOS and (((NMOS near5 PMOS)near5 height)same (equal\$3 near3 mobility))	0	<u>L3</u>
<u>L2</u>	CMOS and (((NMOSFET near5 PMOSFET)near5 height)same (equal\$3 near3 mobility))	0	<u>L2</u>
<u>L1</u>	CMOS and (height same (equal\$3 near3 mobility))	0	<u>L1</u>

END OF SEARCH HISTORY



[Web](#) [Images](#) [Groups](#) [News](#) [Froogle](#) [Local](#) [more »](#)

[Advanced Search](#)

[Preferences](#)

The "AND" operator is unnecessary — we include all search terms by default. [\[details\]](#)

**Web** Results 1 - 1 of 1 for **cmos and "width" of MOSFET and PMOSFET and "mobility adjustment"** . (0.35 :

Tip: Try removing quotes from your search to get more results.

[PDF](#) **CMOS Circuit Performance Enhancement by Surface Orientation ...**

File Format: PDF/Adobe Acrobat

improving pMOSFET device performance. However, practical application of such a scheme has ... based **mobility adjustment** has a larger effect on performance ...  
[ieeexplore.ieee.org/iel5/16/29495/01337173.pdf?arnumber=1337173](http://ieeexplore.ieee.org/iel5/16/29495/01337173.pdf?arnumber=1337173) - [Similar pages](#)



Google Desktop Search

9:30 AM

Free! Instantly find your email, files, media and web history. [Download now](#)

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

©2005 Google

## Refine Search

### Search Results -

Terms	Documents
cmos and mobility and aspect ratioL10	0

**Database:**

- US Pre-Grant Publication Full-Text Database
- US Patents Full-Text Database
- US OCR Full-Text Database
- EPO Abstracts Database
- JPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

**Search:**

### Search History

**DATE:** Friday, July 08, 2005 [Printable Copy](#) [Create Case](#)

<u>Set</u>	<u>Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set</u>
				<u>Name</u>
side by side				result set
DB=TDBD; PLUR=YES; OP=OR				
<u>L11</u>	cmos and mobility and aspect ratio	L10	0	<u>L11</u>
<u>L10</u>	L9		0	<u>L10</u>
DB=DWPI; PLUR=YES; OP=OR				
<u>L9</u>	L8		0	<u>L9</u>
DB=JPAB; PLUR=YES; OP=OR				
<u>L8</u>	L7		0	<u>L8</u>
DB=EPAB; PLUR=YES; OP=OR				
<u>L7</u>	L5		0	<u>L7</u>
<u>L6</u>	L5		0	<u>L6</u>
DB=PGPB; PLUR=YES; OP=OR				
<u>L5</u>	L4		0	<u>L5</u>
DB=USPT; PLUR=YES; OP=OR				
CMOS and (((NMOS near5 PMOS)near5 (aspect ratio)) same (equal\$3				

<u>L4</u>	near3 mobility))	0	<u>L4</u>
<u>L3</u>	CMOS and (((NMOS near5 PMOS)near5 height)same (equal\$3 near3 mobility))	0	<u>L3</u>
<u>L2</u>	CMOS and (((NMOSFET near5 PMOSFET)near5 height)same (equal\$3 near3 mobility))	0	<u>L2</u>
<u>L1</u>	CMOS and (height same (equal\$3 near3 mobility))	0	<u>L1</u>

END OF SEARCH HISTORY



Web Images Groups News Froogle Local more »

cmos and "aspect ratio" and "mobility" of MOS

[Advanced Search](#)  
[Preferences](#)

The "AND" operator is unnecessary — we include all search terms by default. [\[details\]](#)

**Web** Results 11 - 20 of about 49 for **cmos and "aspect ratio" and "mobility" of MOSFET and PMOSFET**. (0.

**[PDF]** [Advanced Gate Stack Materials and Processes for sub-70 nm CMOS ...](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)

changes in the materials used in the **MOSFET** gate stack will become necessary as will

... Sub-70 nm elevated source/drain bulk-silicon **PMOSFET** devices have ...

[www.eecs.berkeley.edu/~tking/theses/pranade.pdf](#) - [Similar pages](#)

[ More results from [www.eecs.berkeley.edu](#) ]

**[PDF]** [1.J electronic devices](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)

from a bulk germanium **PMOSFET**. The **mobility** is ... stration of a  $\epsilon$ Si p-MOSFET

that exhibits much higher, effective **mobility** in strong inversion than those ...

[www-mtl.mit.edu/research/annual\\_reports/2003/elec\\_devices.pdf](#) - [Similar pages](#)

**清华大学微电子学研究所介绍**

2 为了研究器件参数对GeSi **MOSFET**器件性能的影响，本文在建立一个简单的GeSi **MOSFET**

... 为了更细致、精确地进行分析，我们分别对GeSi **PMOSFET** 和GeSi **NMOSFET** ...

[dns.ime.tsinghua.edu.cn/yjkf/lunwen/2000.htm](#) - 152k - [Cached](#) - [Similar pages](#)

**Introduction of IMETU**

The influence of GeSi **MOSFET** longitudinal structure on device performance ...

Investigations of QMEs in a **pMOSFET** are classified into two approaches: ...

[dns.ime.tsinghua.edu.cn/english/research/papers/2000.htm](#) - 422k - [Cached](#) - [Similar pages](#)

[ More results from [dns.ime.tsinghua.edu.cn](#) ]

**[PDF]** [WWoorkksshoopp oonn SSeemmiiccoonndduucc toorrssaannddMMiiccrr o](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)

-Aluminum oxynitride gate **pMOSFET** formed by sputtering DC ... depends on the required **aspect ratio** of the structure. Most of the ...

[www.ccs.unicamp.br/seminatec/proceedings\\_seminatec2005.pdf](#) - [Similar pages](#)

**[PDF]** [RELIABILITY PHYSICS SYMPOSIUM PRELIMINARY PROGRAM](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... 30nm gate length ultra- high **aspect ratio** FinFETs—WS ... k post-deposition cleaning in improving **CMOS** bias instabilities and **mobility** a potential ...

[www.irps.org/05-43rd/pp2005\\_747web.pdf](#) - [Supplemental Result](#) - [Similar pages](#)

[ More results from [www.irps.org](#) ]

**[PDF]** [Page 1 0 ...](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)

**mobility** of carriers in the channel of the **MOSFET**, and therefore limit the ...

Several papers on **MOSFET** design for 0.05-0.1 micron **CMOS** were presented. ...

[www.atip.org/public/atip.reports.99/atip99.057r.pdf](#) - [Similar pages](#)

**Use our search function to find references for your applications ...**

A 1  $\mu$ m gate-length depletion-mode n-channel GaAs **MOSFET** with an Al<sub>2</sub>O<sub>3</sub> gate ...

An excellent step coverage and a large **aspect ratio** (similar to 40:1) prove ...

[www.cambridegenanotech.com/cgi-bin/Search.cgi?mode=example&example=al2o3](#) - 292k -

[Cached](#) - [Similar pages](#)

[PDF] [JOPAL 1999](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)

Enhanced mobility PMOSFET's using tensile-strained Si ... aspect ratio via etch  
in a high density, plasma etch tool. H01L 021/28 ...

[www.wipo.org/scit/en/jopal/1999/oct.pdf](http://www.wipo.org/scit/en/jopal/1999/oct.pdf) - [Similar pages](#)

[News Letter](#) - [ [Translate this page](#) ]

Nano CMOS 소자모델의 내부 파라미터에 대한 바이어스 종속 데이터 추출 ... High-k  
oxide MOSFET 소자용 ultrashallow plasma doping 및 저온 열처리 공정 연구 ...

[www.nanotech.re.kr/newsletter/20040402\\_3.htm](http://www.nanotech.re.kr/newsletter/20040402_3.htm) - 132k - [Cached](#) - [Similar pages](#)

◀ Goooogle ▶

Result Page: [Previous](#) [1](#) [2](#) [3](#) [4](#) [Next](#)

[Search within results](#) | [Language Tools](#) | [Search Tips](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

©2005 Google